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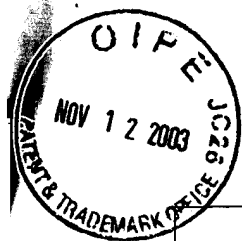
TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/732,181
	Filing Date	12/6/00
	First Named Inventor	Steven Teig
	Group Art Unit	2825
	Examiner Name	Leigh M. Garbowski
Total Number of Pages in This Submission	Attorney Docket Number	SPLX.P0003

Enclosures (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Or Individual name	Mani Adeli, Reg. No. 39,585 of Stattler Johansen & Adeli LLP	
Signature		
Date	November 9, 2003	

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PATENT
Docket No. SPLX.P0003

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Mani Aden

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig

Serial No.: 09/732,181

Filing Date: 12/6/00

For: RECURSIVE PARTITIONING
PLACEMENT METHOD AND
APPARATUS

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INFORMATION DISCLOSURE
STATEMENT UNDER 37 C.F.R. § 1.97

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Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit with this Information Disclosure Statement the attached Form PTO-1449, and copies of the documents listed in the 1449 form for consideration by the Examiner. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

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This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

This Information Disclosure Statement is submitted: After receipt of a first Office Action on the merits but before a final Office Action or Notice of Allowance. A fee is required. A fee in the amount of \$180 is enclosed.

If this fee is separated from this IDS or if the Patent Office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 50-1128** referencing SPLX.P0003. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

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Respectfully submitted,

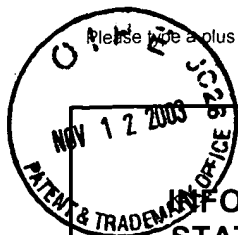
By: _____

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	09/732,181
				Filing Date	12/6/00
				First Named Inventor	Steven Teig
				Group Art Unit	2825
				Examiner Name	Leigh M. Garbowski
Sheet	1	of	7	Attorney Docket Number	SPLX.P0003

U.S. PATENT DOCUMENTS						
Examiner* Initials	Cite No. ¹	U.S. Patent Document		Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	1.	2001/0009 031	A1	07-19-2001	Nitta et al.	
	2.	2002/0182 844	A1	12-05-2002	Igarashi et al.	
	3.	2003/0025 205	A1	02-06-2003	Shively	
	4.	2003/0005 399	A1	01-02-2003	Igarashi et al.	
	5.	4,593,363		06-03-1986	Burstein et al.	
	6.	4,855,929		08-08-1989	Nakajima	
	7.	5,375,069		12-20-1994	Satoh et al.	
	8.	5,587,923		12-24-1996	Wang	
	9.	5,618,744		04-08-1997	Suzuki et al.	
	10.	5,633,479		05-27-1997	Hirano	
	11.	5,637,920		06-10-1997	Loo	
	12.	5,640,327		06-17-1997	Ting	
	13.	5,663,891		09-02-1997	Bamji et al.	
	14.	5,757,089		05-26-1998	Ishizuka	
	15.	5,784,289		07-28-1998	Wang	
	16.	5,798,936		08-25-1998	Cheng	
	17.	5,838,583		11-1998	Varadarajan et al.	
	18.	5,889,677		03-30-1999	Yasuda et al.	
	19.	5,980,093		11-09-1999	Jones et al.	
	20.	6,070,108		05-30-2000	Andreev et al.	

Examiner Signature	Date Considered
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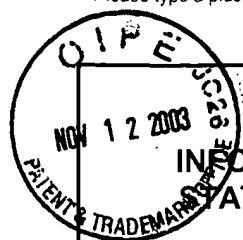
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number Filing Date First Named Inventor Group Art Unit Examiner Name Attorney Docket Number				09/732,181	
				12/6/00	
				Steven Teig	
				2825	
				Leigh M. Garbowski	
Sheet	2	of	7		

U.S. PATENT DOCUMENTS

	21.	6,128,767		10-03-200	Chapman	
	22.	6,209,123	B1	03-27-2001	Maziasz et al.	
	23.	6,253,363	B1	06-26-2001	Gasarov et al.	
	24.	6,295,634	B1	09-25-2001	Matsumoto	
	25.	6,324,674	B2	11-27-2001	Andreev et al.	
	26.	6,324,675	B1	11-27-2001	Dutta et al.	
	27.	6,327,693	B1	12-04-2001	Cheng et al.	
	28.	6,330,707		12-11-2001	Shinomiya et al.	
	29.	6,378,121	B1	04-23-2002	Hiraga	
	30.	6,407,434	B1	06-18-2002	Rostoker et al.	
	31.	6,412,102	B1	06-25-2002	Andreev et al.	
	32.	6,415,422	B1	07-02-2002	Mehrotra et al.	
	33.	6,442,743	B1	08-27-2002	Sarrafzadeh et al.	
	34.	6,448,591	B1	09-10-2002	Juengling	
	35.	6,473,891	B1	10-29-2002	Shively	
	36.	6,480,991	B1	11-12-2002	Cho et al.	
	37.	6,490,713	B2	12-03-2002	Matsumoto	
	38.	6,519,751	B2	02-11-2003	Sriram et al.	
	39.	6,543,043	B1	04-01-2003	Wang et al.	
	40.	6,546,540	B1	04-08-2003	Igarashi et al.	
	41.	6,557,145	B2	04-29-2003	Boyle et al.	
	42.	6,567,967	B2	05-20-2003	Greidinger et al.	

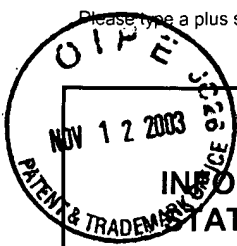
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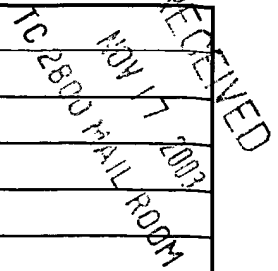
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	09/732,181
				Filing Date	12/6/00
				First Named Inventor	Steven Teig
				Group Art Unit	2825
				Examiner Name	Leigh M. Garbowski
Sheet	3	of	7	Attorney Docket Number	SPLX.P0003

**FOREIGN PATENT DOCUMENTS**

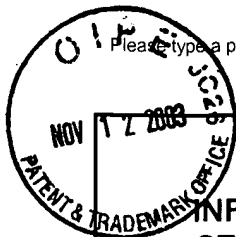
Examiner* Initials	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T 6
		Office ³	Number ⁴	Kind Code (if known)				
	43.	JP	411296560	A	Matsumoto et al.	10-29-1999	with English translation of Abstract;	
	44.	JP	2000-82743	A	Igarashi et al.	03-21-2000	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	45.	JP	H03-173471	A	Tawada et al.	07-26-1991	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	46.	JP	H05-243379	A	Masako Kubota	09-21-1993	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	47.	JP	H05-102305	A	Akihiro Sato	04-23-1993	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	48.	JP	H07-86407	A	Shinpei Miura	03-31-1995	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	4	of	7	Application Number	09/732,181
				Filing Date	12/6/00
				First Named Inventor	Steven Teig
				Group Art Unit	2825
				Examiner Name	Leigh M. Garbowski
				Attorney Docket Number	SPLX.P0003

FOREIGN PATENT DOCUMENTS

49.	JP	H09-162279	A	Masaaki Yoshida	06-20-1997	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
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STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet	5	of	7	Application Number	09/732,181
				Filing Date	12/6/00
				First Named Inventor	Steven Teig
				Group Art Unit	2825
				Examiner Name	Leigh M. Garbowski
				Attorney Docket Number	SPLX.P0003

FOREIGN PATENT DOCUMENTS**NON PATENT LITERATURE DOCUMENTS**

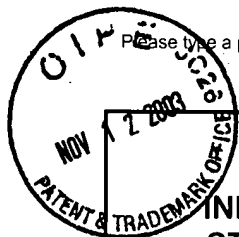
Examiner* Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T 6
	50.	Ahuja, R. et al., Faster Algorithms for the Shortest Path Problem, Journal of the Association for Computing Machinery, vol. 37, No. 2, April 1990, pp. 213-223.	
	51.	Brady, L. et al., Channel Routing on a 60° Grid, extended abstract, pp. 956-931.	
	52.	Brambilla, A. et al., Statistical Method for the Analysis of Interconnects Delay in Submicrometer Layouts, IEEE, 8/2001, pp. 957-966.	
	53.	Carothers, K., A Method of Measuring Nets Routability for MCM's General Area Routing Problems, 1999, pp. 186-192.	
	54.	Chen, H. et al., Physical Planning of On-Chip Interconnect Architectures, 2002, IEEE, International Conference, pp. 30-35.	
	55.	Cheng, K. et al., Manhattan or Non Manhattan? A Study of Alternative VLSI Routing Architectures, pp 47-52, 2000.	
	56.	Cheng, K., Steiner Problem in Octilinear Routing Model, a Thesis Submitted for the Degree of Master of Science, National University Singapore, 1995, pp. 1-122.	
	57.	Chip Model with Wiring Cost Map, August 1983, IBM Technical Disclosure Bulletin, vol. 26, iss. 3A, pp. 929-933.	
	58.	Cong, J. et al., Efficient Heuristics for the Minimum Shortest Path Steiner Arborance Problem with Applications to VLSI Physical Design, Cadence Design Systems and UCLA Computer Science Department, pp.88-95.	
	59.	Cong, J. et al., Performance-Driven Multi-Layer General Routing for PCB/MCM Designs, UCLA Computer Science Department, 1998, pp. 356-361.	
	60.	Enbody, R. et al., Near-Optimal n-Layer Channel Routing, 23 rd Design Automation Conference, 1986, pp. 708-714.	
	61.	Fang, S. et al., Constrained Via Minimization with Practical Considerations for Multi-Layer VLSI/PCB Routing Problems, 28 th ACM/IEEE Design Automation Conference, 1991, pp. 60-65.	
	62.	Hom, I. et al., Estimation of the Number of Routing Layers and Total Wirelength in a PCB Through Wiring Distribution Analysis, 1996, pp. 1-6.	

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Sheet

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7

Application Number 09/732,181

Filing Date 12/6/00

First Named Inventor Steven Teig

Group Art Unit 2825

Examiner Name Leigh M. Garbowski

Attorney Docket Number SPLX.P0003

FOREIGN PATENT DOCUMENTS

- | | |
|-----|---|
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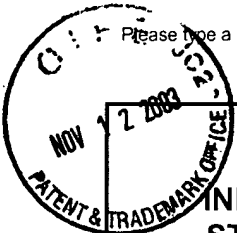
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Application Number	09/732,181
Filing Date	12/6/00
First Named Inventor	Steven Teig
Group Art Unit	2825
Examiner Name	Leigh M. Garbowski
Attorney Docket Number	SPLX.P0003

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